Intel® FPGA Programmable Acceleration card (Intel FPGA PAC)

Platform Selector Guide

Features		Intel PAC with INTEL Arria® 10 GX FPGA	Intel FPGA PAC N3000	Intel FPGA PAC D5005
Physical Dimension	Width	Single slot	Single slot	Dual slot
	Form Factor	½ length, full height ½ length, ½ height	½ length, full height	¾ length, Full height
	Weight	255 g	364 g	1,000 g
Memory	DDR Format	4GB DDR4 SDRAM	4 GB DDR4 SDRAM	8 GB DDR4 SDRAM (RDIMM)
	DDR Total Capacity	8 GB	9 GB	32 GB
	DDR Maximum Data Rate	2133MT/s	2400 MT/s	2400 MT/s
	QSPI Flash Memory	1 Gb	2 Gb	2 Gb
	SRAM Total Capacity	-	144 Mb QDR IV	-
Interfaces and Modules	PCI Express*	Gen 3x8 (electrical) Gen 3x16 (mechanical)	Gen 3x16	Gen 3x16
	Network Interface	1 x QSFP+	2 x QSFP28	2 x QSFP28
	USB Interface	USB 2.0	-	USB 2.0
	Dual Intel Ethernet Converged Network Adapter XL710	No	Yes	No
	FPGA Interface Manager	Yes	Yes	Yes
Power	Typical Power Consumption	45 W	45 W	189 W
	Maximum Power Consumption (TDP)	66 W	100 W	215 W
	Power Management: Intel Enpirion® Power Solutions	Yes	Yes	Yes
FPGA Resources	FPGA	Intel Arria 10 GX	Intel Arria 10 GT	Intel Stratix® 10 SX
	Logic Elements	1,150,000	1,150,000	2,753,000
	Adaptive Logic Modules (ALMs) Registers	1,708,800	1,708,800	3,732,480
	On-chip Memory	65.7 Mb	65.7 Mb	244 Mb
	DSP Blocks	3,036	3,036	11,520
Tools Support	Intel Acceleration Stack for Intel Xeon® CPU with FPGAs	Yes	Yes	Yes
	Intel Quartus® Prime Software	Yes	Yes	Yes
	Open Programmable Acceleration Engine (OPAE)	Yes	Yes	Yes
	Data Plane Developer Kit (DPDK)	-	Yes	-
	Intel Distribution of OpenVINO™ Toolkit	Yes	-	-